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### **PROFESSIONAL LICENSE**

Registered Professional Engineer in Electrical Engineering  
Certificate No. E19741, State of California

### **SUMMARY**

Over 20 years experience in research and development of electronic equipment for the Telecommunications industry: telecommunications standards, product development cycle, software development, hardware development, reverse engineering.

### **SKILLS**

#### IN EMBEDDED

- C, C++ programming
- Hardware Design and Development experience: FPGA, digital and analogue circuit design.
- Multitasking, ASIC drivers, embedded SW, platform software and hardware bring up.
- Real Time Operating Systems: INTEGRITY, vxWorks and pSOS and development tools.
- Broad experience in software development for xDSL technologies.
- Experience with Ethernet technologies.
- Experience with TDM telephony, SIP, ATM, IP suite
- Motorola Microcontrollers: MPC8260, MPC860, 68302, 68360.
- Broad experience generating formal documentation from Requirements to Detailed Design Specification
- Test equipment Experience: broad range of Networking test equipment such as Spirent's Adtech, Smartbits, T-bird and Telecom (Network Analyzers, ..) and hardware support equipment

#### IN JAVA

- Network Management for Access Products
- Java Programming
- Server-side technologies: JBoss, Hibernate, JCR (Jackrabbit), Spring, Mysql

## PROFESSIONAL EXPERIENCE

### **Alcatel-Lucent, Petaluma, CA USA**

#### *Network Management*

#### *Technical Team Lead*

*March 2008 – Present*

Software Lead in a multinational team developing Alcatel-Lucent AMS5520 Network Management product. I have provided expertise in Network Security, Performance, Fault and Configuration Management.

Technical Team Lead for Alcatel-Lucent 5529 IDM Inventory Data Manager Product, which provides a centralized repository of network information and outstanding query capabilities via GUI and OSS NBI.

Performed development in Java, employing open source technologies like JBoss, Java Content Repository, Hibernate, MySql database on the server side, and Eclipse RCP on the client side.

### **Alcatel, Petaluma, CA USA**

#### *Circuit Emulation Services over Ethernet*

#### *Senior Software Development Engineer*

*July 2006 – March 2008*

Did software development of Circuit Emulation Service (CES) application for Alcatel Litespan project. This application enables the transport of TDM services over an Ethernet network, using SNMP management to configure and obtain statistics on the CES streams.

### **Alcatel, Petaluma, CA USA**

#### *xDSL, T1, IMA and ATM Technologies*

#### *Senior Software Development Engineer*

*Jan 2004 – July 2006*

Software development of ADSL (including ADSL2+ and READSL), IMA and DS1 applications for the Alcatel 7329RU product (remote DSLAM with ADSLx lines and 4 T1 transport lines over HDSL4 intended to provide broadband service in remote areas in the US market).

### **Alcatel, Petaluma, CA USA**

#### *GR303,TR-08, SONET Technologies*

#### *Technical Team Lead*

*Aug 2000 – Dec 2004*

Was team leader on GR303, T1, T0 and SONET bandwidth management for Alcatel 7201 Litespan Multi Service Access Platform.

Software lead for Hardware/Software integration Alcatel 7201 Litespan switching modules.

### **Alcatel, Madrid, Spain**

#### *ISDN, POTS Technologies*

#### *Software Development Engineer*

*Jan 1996 – Aug 2000*

Did device driver development (Motorola PowerQuick over VxWork) for Alcatel A9900 EVOLIUM™ LMDS.

Software design and development of ISDN service for Alcatel A9800R3 fixed radio access system over pSOS OS, including drivers, application SW and production test SW.

**Alcatel and Telettra, Madrid, Spain**

*Radio access products*

*Hardware Development Engineer*

*July 1990 – Jan 1996*

Design of radio baseband modules for A9800R1 and SMD-30 systems.

Design of the Base Band Board for a GSM Fixed Cellular Terminal.

Design of a PCM/ADPCM conversion board for both the A9800 and the SMD-30 radio access systems.

Hardware design and development of a line-testing board used to test POTS subscriber lines in A9800R2 system.

Hardware design of Control and Maintenance board for SMD-30 radio access system.

**EDUCATION**

University 2005-2008	MS in Computer Science Sonoma State University (California, USA) <a href="http://www.sonoma.edu/">http://www.sonoma.edu/</a>
University 1985-1990	Ingeniero Tecnico de Telecomunicacion (Especialidad Equipos Electronicos) University de Alcalá de Henares (Madrid, Spain) <a href="http://www.uah.es/">http://www.uah.es/</a>